

METHOD AND CIRCUIT FOR ADJUSTING A REFERENCE VOLTAGE SIGNAL

ABSTRACT OF THE DISCLOSURE

A voltage trim circuit, in accordance with one embodiment of the invention, includes an operational amplifier, a transistor, a voltage divider and a bias current circuit. The operational amplifier is coupled to an input. The transistor is coupled to the operational amplifier and a first potential. The voltage divider circuit is coupled to the operational amplifier, the transistor and an output. The bias current circuit is coupled to the voltage divider circuit and a second potential. The voltage divider generates an output voltage as a function of a selectable divider ratio and provides a substantially constant feedback path to the operational amplifier. The bias current circuit provides for selectively adjusting a load resistance of the transistor to maintain a substantially constant load current through the transistor.